a sorter coupled to the search array to perform one or more contextual searches on content in the search array via parallel pattern matching in response to executing one or more search instructions specifying the one or more pattern searches and presenting one or more patterns to the content, wherein the parallel pattern matching comprises performing a simultaneous search within all M rows for all of the N search pattern elements input by the N pattern input lines, all in one clock period.

Claims 2-25 and 27-66:

Please also amend depending **claims 2-25 and 27-66** (now 26-65) to recite, in the first line, **"The system defined"** rather than "The rule processor defined". Further note that claim 47 (now claim 46) is amended to read "The system of claim 1" rather than "The rule processor of claim 1" in the first line.

Claim 14 has been amended as:

Claim 14. The ~~rule processor~~ system defined in Claim 12 wherein a value to which the pointer points is a result of a previously performed search by ~~the~~ a search execution hardware.

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Claim 66 has been amended as:

A process comprising:

loading a set of input payload search registers with content data;